Atty. Dkt. No. 016907-1575

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Haruki TODA et al.

Title: CLOCK-SYNCHRONOUS SEMICONDUCTOR MEMORY DEVICE

Appl. No.: Unassigned

Filing Date: August 19, 2003

Examiner: Unassigned

Art Unit: Unassigned

UNDER 37 CFR §1.56

Commissioner for Patents Box PATENT APPLICATION Washington, D.C. 20231

Sir:

Applicants submit herewith on Form PTO-1449 a listing of the documents cited by or submitted to the U.S. PTO in the parent application Serial No. 10/310,797, filed December 6, 2002. As provided in 37 CFR §1.98(d), copies of the documents are not being provided since they were previously submitted to or cited by the United States Patent & Trademark Office in the above-identified application.

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present application or that such document is considered material to patentability as defined in 37 CFR §1.56(b). Applicants do not waive any rights to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a *prima facie* art reference against the claims of the present application.

TIMING OF THE DISCLOSURE

The listed documents are being submitted in compliance with 37 CFR §1.97(b), within three (3) months of the filing date of the application.

RELEVANCE OF EACH DOCUMENT

The relevance of the foreign-language documents is explained in the parent application.

Applicants respectfully request that any listed document be considered by the Examiner and be made of record in the present application and that an initialed copy of Form PTO-1449 be returned in accordance with MPEP §609.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 CFR §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Date August 19, 2003

FOLEY & LARDNER

Customer Number: 22428

22428
PATENT TRADEMARK OFFICE

Telephone: (202) 672-5426 Facsimile: (202) 672-5399 Respectfully submitted,

Glenn Law

Attorney for Applicant Registration No. 34,371

Sheet 1 of 3										
FORM PSO 1449 (modified)							SERIAL NO. Unassigned			
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				APPLICANT Haruki TODA et al.						
LIST OF REFERENCES CITED BY APPLICANT(S) (Use several sheets if necessary)				FILING DATE GROUP						
Date Submitted to PTO: August 19,			2003	003 August 19, 200				Unassigned		
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*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME		CLASS		SUBCLASS	SUBCLASS FILING DATE IF APPROPRIATE	
	A1	4,330,852	05/18/82	Redwine et al.		365		221 11/23/79		
	A2	4,819,213	04/04/89	Yamaguchi et al.		365		233 12/11/86		
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	A11	0 315 194	05/10/89	Europe			-			
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	A18		Ohno, Chikal, "Self-Timed RAM: STRAM", FUJITSU Sci. Tech. J., 24, 4, December 1988, pp. 293-300.						300.	
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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not consider include copy of this form with next communication to applicant.

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LIST OF RE	FERENCE	S CITED BY APPLIC	ANT(S)	Haruki TODA et	(al.					
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	A21	5,390,149	02/14/95	Vogley et al.	Vogley et al.		365 189.01			
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